



This certificate attests that

Joseph DINIUS

has successfully completed the following 24-hour training course

**Embedded System Design for the Zynq
UltraScale+ MPSoC
V2019.1**

August 18 - 20, 2020

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ENGINEERING | IP PRODUCTS | TRAINING

A handwritten signature in blue ink, reading 'Reg Zatrepałek', written over a light blue rectangular background.

Reg ZATREPALEK
Instructor